

IN THE UNITED STATES PATENT OFFICE

In Re Patent Application of

First Named Inventor: Zerbe, Jared L.

Application No.: 09/993,138

Filed: 11/16/2001

For: SIGNAL LINE ROUTING TO REDUCE
CROSSTALK EFFECTS

Examiner: JAMAL, Alexander

Art Unit: 2614

Confirmation 6177
No.

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Comments on Statement of Reasons For Allowance

Sir:

Applicant respectfully enters remarks on the Detailed Action mailed 12/07/2009 as set forth below.

Comments on Detailed Action

Applicant respectfully points out an inconsistency with the language set forth in paragraph 1 of the “Detailed Action” mailed 12/07/09, and comments made during prosecution of the instant case. The action alleges that applicant set forth “comments that there is a correlation between the claimed ‘segment’ and actual traces routed on a PCB (including the inherently required vias) . . .” In fact, in a previous Response, dated October 21, 2009, applicant explained that the specification describes various embodiments where segments may be in the same plane, or in different planes. In no event did the Applicant comment that there is a correlation between the claimed ‘segment’ and actual traces routed on a PCB.


If a telephone interview would be helpful in any way, the examiner is invited to call the undersigned attorney.

Authorization is hereby given to charge deposit account 501914 for any fee due in connection with this Amendment.

Respectfully submitted,

MAHAMEDI PARADICE KREISMAN LLP

Date: 3/4/10


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